CB60-BX

rev. AG+

DIP Switch Settings of the Processors

Processor		Frequency Ratio	SW1	Processor		Frequency Ratio	SW1
66MHz	100MHz			66MHz	100MHz		
300MHz	Future processor	4.5x		433MHz	Future processor	6.5x	
333MHz	Future processor	5x		466MHz	Future processor	7x	
366MHz	Future processor	5.5x		500MHz	Future processor	7.5x	
400MHz	Future processor	6x		Future processor	Future processor	8x	

Black rectangle denotes the part that is protruding, the "adjustable" switch
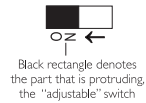
In the example above:

Switch 1: Off
Switch 2: On
Switch 3: Off
Switch 4: On

Note • The voltage regulator will automatically be set according to the voltage of the processor.
• You cannot overclock an Intel Celeron™ processor (PPGA) because its frequency ratio is fixed by the manufacturer. The table above is for factory use only.

JP2 (CPU's FSB) - 1-2 On: Auto (default) ; 2-3 On: 66MHz ; 1-2-3 Off: 100MHz

Note • The "100MHz" setting is reserved for future 100MHz FSB processors. If your system is installed with a 66MHz FSB processor, do not move the jumper cap from its default setting which is pins 1 and 2 On; otherwise your system will not boot.

JP3 (Clear CMOS Data) - 1-2 On: Normal (default) ; 2-3 On: Clear CMOS Data

Note • Before clearing the CMOS data, make sure to power-off your system and unplug the power cord.

JP1(Wake-On-Keyboard/Mouse) - 2-3 On: Enable ; 1-2 On: Disable (default)
By default, JP1 is disabled. Make sure "Keyboard/Mouse Power On" in the Integrated Peripherals setup of the Award BIOS is also disabled. If JP1 was previously enabled with a password set in the "KB Power On Password" field, and now you wish to disable the password(Wake-On-Keyboard) function, make sure to disable the password in this field prior to setting JP1 to disabled. You will not be able to boot up the system if you fail to do so.