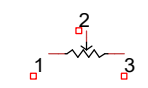


Wire Notes
 Wires on R_POT pinout 2 should be connected directly to each 100K ohm resistor (rather than the capacitor), otherwise ohm readings on the R_POT will be quite off.

In general, around the 5532 or equivalent opamp circuit area, wires should be as short (but not too short) and close to their respective components as possible to prevent parasitic oscillation (interference).

General Notes
 R_POT pinout goes from left to right on the following diagram:



RP1A is the bottom row on the original PCB, while RP1B is the top row, reflected on PCB top view. However, when the finished unit is facing you, potentiometer in view, RP1A is the front row, and RP1B is the rear row.

Shell = shield (regional differences).

| | | | |
|----------|---|--|------------|
| Title | | Chinese Classic 47 Variant [Reverse Engineered 47-PB0 DA1] | |
| Author | | Clarissa Walker | |
| File | C:\Users\CW Cyrix\D ... classic-47_final7.dsn | Document | NE5532 |
| Revision | 1.6 | Date | 2022/01/08 |
| | | Sheets | 1 of 1 |