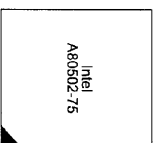
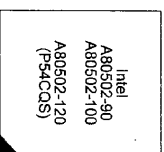


### Bus Clock Jumper Setting

**P54C-75 MHz : PCLK/3**



**P54C-90/100/120 MHz : PCLK/4**



## **Chapter 3**

## System Memory

The PT-2000 can be equipped with the necessary memory for running all your applications. Memory comes in the form of DRAM (SIMMs) and cache SRAM. This chapter describes these two types of memory and gives instructions on how to install each type on the motherboard.

## Memory Locations

The board layout below shows the locations of the DRAM memory banks and the cache SRAM:

