

PNP/PCI Configuration

ROM PC/ISA BIOS (2A6LFF09)			
PNP/PCI CONFIGURATION			
AWARD SOFTWARE, INC.			
PNP OS Installed	:	No	
Resources Controlled By	:	Auto	
Reset Configuration Data	:	Disabled	
	:	CPU to PCI Write Buffer	: Enabled
	:	PCI Dynamic Bursting	: Enabled
	:	PCI Master 0 WS Write	: Enabled
	:	PCI Delay Transaction	: Enabled
	:	PCI#2 Access #1 Retry	: Disabled
	:	AGP Master 1 WS Write	: Enabled
	:	AGP Master 1 WS Read	: Disabled
	:	PCI IRQ Activated By	: Level
	:	Assign IRQ For USB	: Disabled
	:	Assign IRQ For VGA	: Enabled
	:	Assign IRQ For ACPI	: IRQ10
Esc : Quit		+/- : Select Item	
F1 : Help		PU/PD +/- : Modify	
F5 : Old Values		(Shift)F2 : Color	
F6 : Load BIOS Defaults			
F7 : Load Setup Defaults			

PNP OS Installed

If your operating system is a Plug-and-Play one, such as Windows 95, select Yes. The options are: No (Default), Yes.

Resources Controlled By

If set at Auto, the BIOS arranges all system resources. If there exists conflict, select Manual. The options are: Auto (default), Manual. The manual options of **IRQ- / DMA- assigned to** are: Legacy ISA, PCI/ISA PnP.

Reset Configuration Data

When enabled, allows the system to clear the last BIOS configuration data and reset with the default data.

The options are: Enabled, Disabled (default).

CPU to PCI Write Buffer

When enabled, allows data and address access to the internal buffer of the system controller; so the processor can be released from the waiting state.

The options are: Enabled (Default), Disabled.

PCI Dynamic Bursting

When enabled, the PCI controller allows Bursting PCI transfer if the consecutive PCI cycles come with the address falling in same 1KB space. This improves the PCI bus throughput.
The options are: Enabled (Default), Disabled.

PCI Master 0 WS Write

When enabled, allows a zero-wait-state-cycle delay when the PCI master drive writes data to DRAM. The options are: Enabled (Default), Disabled.

PCI Delay Transaction

Enable this feature to abort the current CPI master cycle and to accept the new PCI master request, it reaccepts the original PCI master and returns the PCI data phase to the original PCI master. The options are: Disabled, Enabled (Default).

PCI#2 Access #1 Retry

If set at Enabled, the AGP (PCI#2) access to PCI (PCI#1) will be retried until the maximal count.
The options are: Enabled, Disabled (Default).

AGP Master 1 WS Write

When set at Enabled, the AGP bus master write access to the DRAMs will add one-wait-state cycle.
The options are: Enabled (Default), Disabled.

AGP Mater 1 WS Read

When set at Enabled, the AGP bus master read access to the DRAMs will add one-wait-state cycle.
The options are: Enabled, Disabled (Default).

PCI IRQ Activated By

We suggest that you set this to its default configuration unless you are a qualified technician.
The options are: Level (Default), Edge.

Assign IRQ For USB

If the USB devices were installed on your system, please set at Enabled for the operating system which support USB functions (such as Windows 95 and Windows 98).

The options are: Enabled, Disabled (Default).

Assign IRQ For VGA

If your PCI VGA card does not need an IRQ, select Disabled; therefore, an IRQ can be released for the system use.

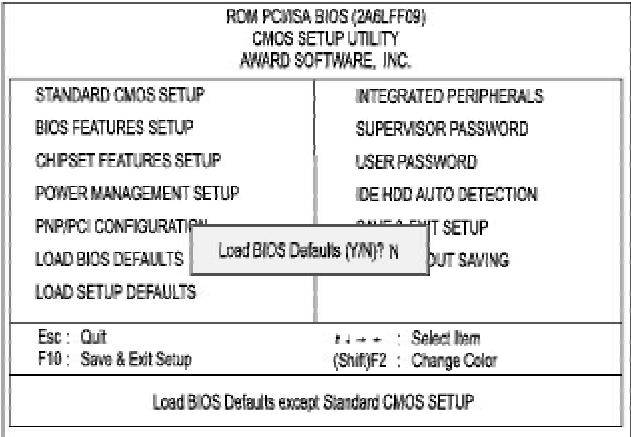
The options are: Enabled, Disabled (Default).

Assign IRQ For ACPI

This feature is provided users with the selection when their operating systems support the ACPI functions (such as Windows 98).

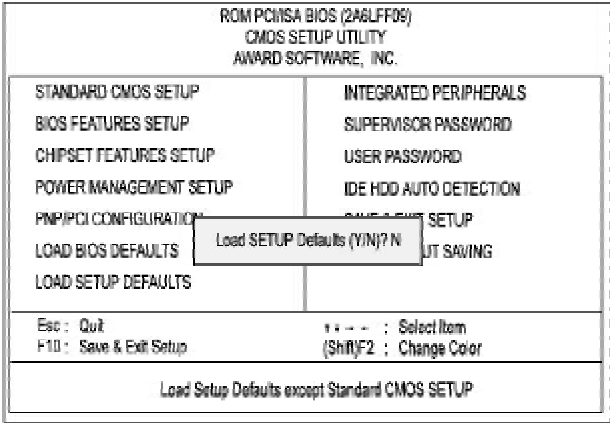
The options are: IRQ10 (Default), IRQ9, IRQ11.

Load BIOS Defaults



BIOS defaults contain the most appropriate values of the system parameters that allow minimum system performance. The OEM manufacturer may change the defaults through MODBIN before the binary image burns into the ROM.

Load Setup Defaults



Selecting *this field* loads the factory defaults for BIOS and Chipset Features which the system automatically detects.

Integrated Peripherals

ROM PC/ISA BIOS (2A6LFF(9)) INTEGRATED PERIPHERALS AWARD SOFTWARE, INC.			
OnChip IDE First Channel	: Enabled	Onboard Parallel Port	: 378/IRQ7
OnChip IDE Second Channel	: Enabled	Onboard Paralle Mode	: SPP
IDE Prefetch Mode	: Enabled	ECP Mode Use DMA	: 3
IDE HDD Block Mode	: Enabled	Parallel Port EPP Type	: EPP1.9
IDE Primary Master PIO	: Auto		
IDE Primary Slave PIO	: Auto		
IDE Secondary Master PIO	: Auto		
IDE Secondary Slave PIO	: Auto		
IDE Primary Master UDMA	: Auto		
IDE Primary Slave UDMA	: Auto		
IDE Secondary Master UDMA	: Auto		
IDE Secondary Slave UDMA	: Auto		
Init Display First	: PCI Stst		
Onboard FDD Controller	: Enabled	Esc: Quit	+/- : Select Item
Onboard Serial Port 1	: 3F8/IRQ4	F1 : Help	PU/PD/+/- : Modify
Onboard Serial Port 2	: 2F8/IRQ3	F5 : Old Values	(Shift)F2 : Color
UART 2 Mode	: Standard	F6 : Load BIOS Defaults	
IR Function Duplex	: Half	F7 : Load Setup Defaults	
RxD , TxD Active	: HL, HI		

OnChip IDE First/Second Channel

The chipset contains a PCI IDE interface with support for two IDE channels. Select Enabled to activate the first and/or second IDE interface. Select Disabled to deactivate an interface, if you install a primary and/or secondary add-in IDE interface.

The options are: Enabled (Default), Disabled.

IDE Prefetch Mode

The onboard IDE drive interfaces supports IDE prefetching, for faster drive accesses. If you install a primary and/or secondary add-in IDE interface, set this field to Disabled if the interface does not support prefetching.

The options are: Enabled (Default), Disabled.

IDE HDD Block Mode

Block mode is also called block transfer, multiple commands, or multiple sector read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

The options are: Enabled (Default), Disabled.

IDE Primary Master PIO

Allows an automatic or a manual configuration of the PCI primary IDE hard disk (master) mode. The options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

IDE Primary Slave PIO

Allows an automatic or a manual configuration of the PCI primary IDE hard disk (slave) mode. The options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

IDE Secondary Master PIO

Allows an automatic or a manual configuration of the PCI secondary IDE hard disk (master) mode. The options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

IDE Secondary Slave PIO

Allows an automatic or a manual configuration of the PCI secondary IDE hard disk (slave) mode. The options are: Auto (Default), Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

IDE Primary Master UDMA

Allows you to select the first PCI IDE channel of the first master hard disk mode or to detect it by the BIOS if the hard disk supports UDMA (Ultra DMA, faster than DMA). The options are: Auto (Default), Disabled.

IDE Primary Slave UDMA

Allows you to select the first PCI IDE channel of the first slave hard disk mode or to detect it by the BIOS if the hard disk supports UDMA (Ultra DMA, faster than DMA). The options are: Auto (Default), Disabled.

IDE Secondary Master UDMA

Allows you to select the second PCI IDE channel of the secondary master hard disk mode or to detect it by the BIOS if the hard disk supports UDMA (Ultra DMA, faster than DMA).

The options are: Auto (Default), Disabled.

IDE Secondary Slave UDMA

Allows you to select the second PCI IDE channel of the secondary slave hard disk mode or to detect it by the BIOS if the hard disk supports UDMA (Ultra DMA, faster than DMA). The options are: Auto (Default), Disabled.

Init Primary Display

When you install an AGP VGA card and/or a PCI VGA card on the board, this feature allows you to select the initiation of the monitor display from which card. The options are: PCI Slot (Default), AGP.

Onboard FDD Controller

When enabled, the floppy diskette drive (FDD) controller is activated. The options are: Enabled (Default), Disabled.

Onboard Serial Port 1

If the serial port 1 uses the onboard I/O controller, you can modify your serial port parameters. If an I/O card needs to be installed, COM3 and COM4 may be needed.

The options are: 3F8/IRQ4 (Default), 3E8/IRQ4, 2F8/IRQ3, 2E8/IRQ3, Disabled.

Onboard Serial Port 2

If the serial port 2 uses the onboard I/O controller, you can modify your serial port parameters. If an I/O card needs to be installed, COM3 and COM4 may be needed.

The options are: 2F8/IRQ3 (Default), 3E8/IRQ4, 2E8/IRQ3, 3F8/IRQ4, Disabled.

UART 2 Mode

Allows you to select the IR modes if the serial port 2 is used as an IR port. Set at Standard, if you use COM2 as the serial port as the serial port, instead as an IR port. The options are: HPSIR, ASKIR, Standard (Default).

IR Function Duplex

This feature is available only if the above item, UART 2 Mode, is set at ASKIR or HPSIR. It allows you to select the infrared data transaction way. The options are: Half (Default), Full.

RxD, TxD Active

This feature is available only if the item, UART 2 Mode, is set at ASKIR or HPSIR. The feature allows you to select the active signals of the reception end and the transmission end. This is for technician use only. The options are: Hi, Hi (Default); Hi, Lo; Lo, Hi; Lo, Lo.

Onboard Parallel Port

Allows you to select from a given set of parameters if the parallel port uses the onboard I/O controller. The options are: 378/IRQ7 (Default), 278/IRQ5, 3BC/IRQ7, Disabled.

Onboard Parallel Mode

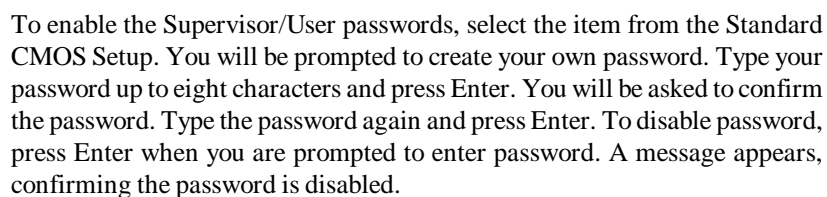
Allows you to connect with an advanced printer. Select SPP for standard parallel port (SPP) used on IBM PC/XT, PC/AT and bi-directional parallel port found on PS/2 system. Select EPP/SPP mode for enhanced parallel port and the standard parallel port. Select ECP mode for Microsoft and HP Extended Capabilities Parallel Port. Select ECP/EPP mode for both ECP and EPP modes. The options are: SPP (Default), EPP/SPP, ECP, ECP/EPP.

ECP Mode Use DMA

If you set the above item, Onboard Parallel Mode, to be ECP or ECP/EPP, this feature allows you to select Direct Memory Access (DMA) channel. The options are: 3 (Default), 1.

Parallel Port EPP Type

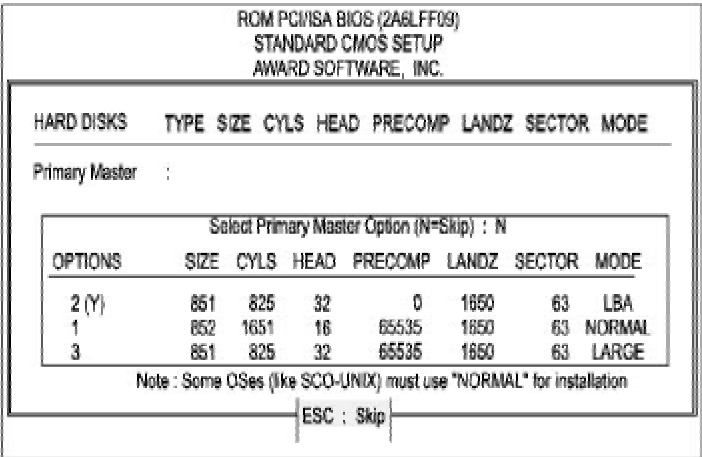
If you set the above item, Onboard Parallel Mode, to be EPP/SPP or ECP/EPP, this feature allows you to select the EPP type version. The options are: EPP1.9 (Default), EPP1.7.



Under the BIOS Feature Setup, if System is selected under the Security Option field and the Supervisor Password is enabled, you will be prompted for the Supervisor Password every time you try to enter the CMOS Setup Utility. If System is selected and the User Password is enabled, you will be requested to enter the User Password every time you reboot the system. If Setup is selected under the Security Option field and the User Password is enabled, you will be prompted only when you reboot the system.

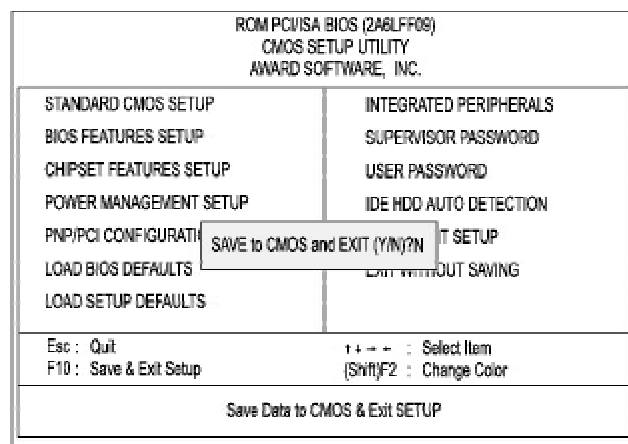
If you forget your password, turn off the system power first and remove the system unit cover. Please refer to Page 2-4, **Clear Password**, on how to clear the password and then reset the system. At this point, you will not be asked for the password to enter Setup.

IDE HDD Auto Detection



The IDE Hard Disk Drive Auto Detection feature automatically configures your new hard disk. Use it for a quick configuration of new hard drives. This feature allows you to set the parameters of up to four IDE HDDs. The option with (Y) are recommended by the system BIOS. You may also keys in your own parameters instead of setting by the system BIOS. After selecting settings, press Esc key to return the main menu. For confirmation, enter the Standard CMOS Setup feature.

Save and Exit Setup



After you have made changes under Setup, press Esc to return to the main menu. Move cursor to Save and Exit Setup or press F10 and then press Y to change the CMOS Setup. If you did not change anything, press Esc again or move cursor to Exit Without Saving and press Y to retain the Setup settings. The following message will appear at the center of the screen to allow you to save data to CMOS and exit the setup utility:

SAVE to CMOS and EXIT (Y/N)?

Exit without Saving

If you select this feature, the following message will appear at the center of the screen to allow you to exit the setup utility without saving CMOS modifications:

Quit Without Saving (Y/N)?

NOTE : Default values of the various Setup items on this chapter may not necessarily be the same ones.

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