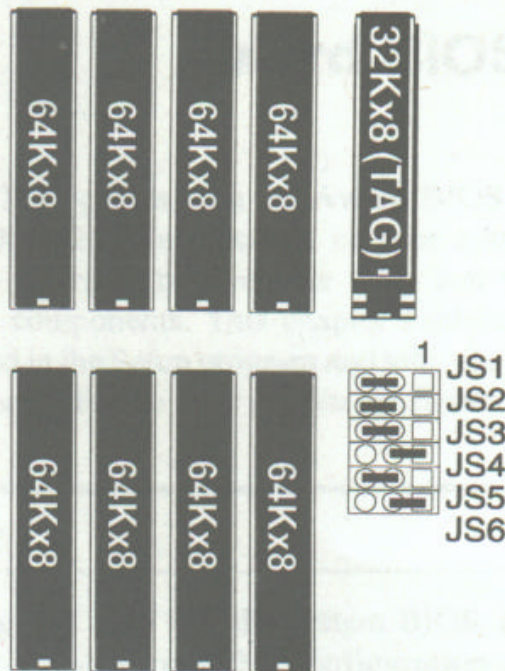


**512K Cache SRAM**



**OR**

