

Regulator Board Installation

This section describes the installation of a regulator board on the mainboard, depending on the S-series CPU to be used.

If an onboard regulator is not present, a regulator may need to be installed before using any S-series processor. Please refer to the instructions below on how to install a regulator. Please also refer to page 2-2 for the correct CPU jumper selection.

→ **NOTE : If you do not intend to install a regulator on the mainboard, place jumper caps onto pins 1-2, 3-4, 13-14 and 15-16 of the CN6 connector. If the CPU is 5 volts, other wise, refer to**

1. Remove jumpers from connector CN6. Mainboard setting 2-3
2. Place the regulator board as shown on the figure below with the correct pin orientation.

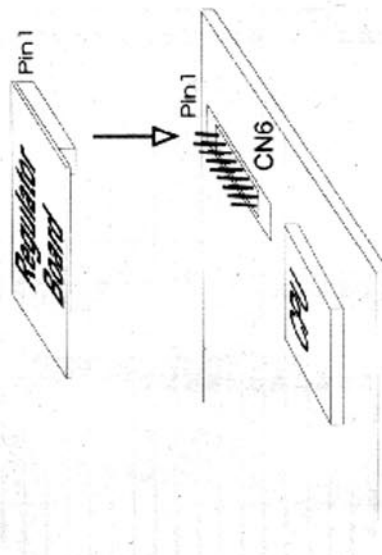


Figure 2-2. Regulator Board Installation

Memory Subsystem

The 486-PVT is equipped with the memory necessary for running all your applications. Memory comes in the form of DRAM (SIMMs) and cache SRAM. This chapter describes these two kinds of memory and gives instructions on how to install each kind on the mainboard.

Memory Locations

The board layout below shows the locations of the DRAM memory banks and the cache SRAM:

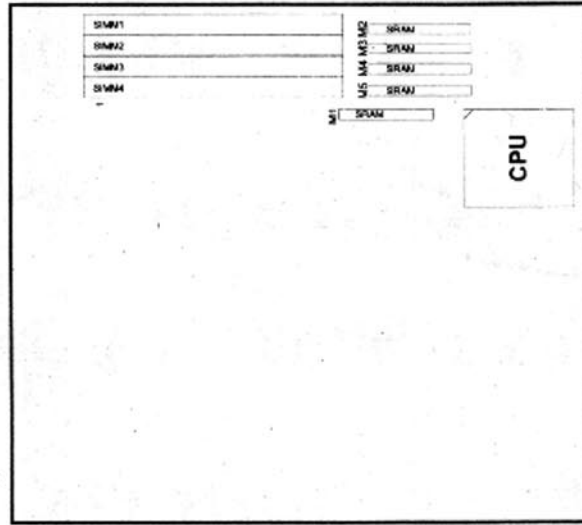


Figure 3-1. Cache and Memory Locations