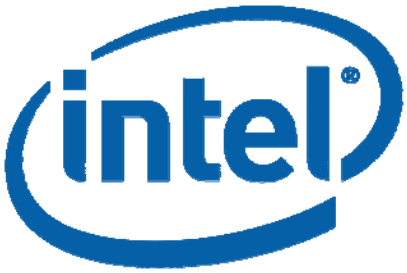

Server WHQL Testing Services

Enterprise Platforms and Services Division

Intel® Server Board S5400SF

Intel® Server System SR1560SFHS-SATA

Intel® Server System SR1560SF-Fixed

Server Test Submission (STS) Report

For the Microsoft® Windows® Logo Program (WLP)

Rev 2.0

May 21, 2008

This report describes the Intel® S5400SF Server Board System Windows* Logo Program test run conducted by Intel Enterprise Platforms and Services Division (EPSD).

Purpose of this WLP Submission (Submission ID: 1291685):

System First Time submission for the Microsoft* Designed for Windows Logo submission for the Intel® Server Board S5400SF.

Submission Type:	Reason for test run	Check one
First Time Submission	Initial Microsoft Designed for Windows logo submission. New product submission.	☑
System Update	Hardware update. (For example, update submission test run with new processor speeds.)	☐
BIOS Update	BIOS and/or Firmware update. (For example, update submission test run with new BIOS to support additional processor speeds.)	☐
OS Update	OS update. (For example, update submission test run to add Microsoft Designed for Windows Server 2008 logo to product.)	☐

Revision History and Disclaimers

Revision History		
Revision	**Date**	**Comments**
1.0	01/11/2008	Internal draft of the STS Report for Windows Server 2003 Submission for Intel® Server System SR1560SFHS-SATA.
1.1	05/20/2008	Updates to document Title and text to include all SKUs for which Submission ID 1276454 applies
2.0	05/20/2008	Internal draft of the STS Report for Windows Server 2008 Submission for Intel® Server Board S5400SF, Intel® Server System SR1560SFHS-SATA and Intel® Server System SR1560SF-Fixed

Contents

Introduction

This report provides an overview of the testing conducted on the Intel® Server System SR1560SFHS-SATA by Intel EPSD and provides details about this testing run.

Overview of Contents

Section	Content
Introduction	Brief descriptions of the sections in the report. Table listing terms and definitions.
Server Product Submission Information	Submission information, ID # and final server board configuration upon completion of WLP including HW, Driver version, BIOS version, and Board AA number
DTM	All DTM tests used during testing
Errata and Contingencies	All Microsoft* errata or contingencies used during testing

Terms and Definitions

Term	Definitions
EPSD	Enterprise Platforms and Services Division
HCL	Windows Hardware Compatibility List. Changed to Windows Server Catalogue. You can view the catalogue at: http://www.microsoft.com/windows/catalog/server/
DTM	Driver Test manager. For latest Server DTM tests visit: http://www.microsoft.com/whdc/hwtest/system/default.mspx
STS	Server Test Submission Report published by EPSD
WHDC	Windows* Hardware and Driver Central. Provides technical information, development and testing kits, newsletters and support information. http://www.microsoft.com/whdc/default.mspx
WHOS	Windows Hardware Online Service – Secure online web site used to submit products for logo qualification and review submission history. https://winqual.microsoft.com/
WHQL	Windows* Hardware Qualification Lab. For more information visit the WHDC home page at: http://www.microsoft.com/whdc/whql/default.mspx
WLP	Windows Logo Program. For further information see: http://www.microsoft.com/whdc/winlogo/default.mspx
WTS	Workstation Test Submission Report published by EPSD

Server System Submission Information

Intel Server System Submission Report: Completion of WLP

Data in this section reflects system submission information at the time of WLP Update submission.

Submission Information

Submission ID		
Submission ID / Master ID	**1291685**	
Submission Type		
	Check Submission Type	**Comments**
First-Time Hardware and Driver Test Submission	☑	
System Update Test Submission	☐	
Product Category		
Hardware Category	PC System or Server	
Operating System family	Windows Server 2008 32 & 64 Bit families	
Product Detail		
General Product Information		
Equipment Type	Server	
Primary Target Market	Business/Corporate	
Compliance	All applicable requirements	
Characterization (optional)		
	Check appropriate options	**Comments**
Web Server	☑	
SQL Database Server	☑	
File Server	☑	

Server Board Configuration Information

Processor	
Quantity	2 (physical processors installed)
Front Side Bus Speed	1333 MHz / 1600 MHz
Family/Model	Intel® Xeon® Quad-core and Dual-Core processors
Speed	
System Memory	
Amount Installed	64 GB
Memory Type	FB DIMM DDR2-667 / DDR2-800 (4 GB FBDIMMs, ECC)
Power Management	
ACPI Sleep States (S1, S2, S3, S4)	S1, S4
Server Board Product	
System uses logo'd motherboard	NO (Server boards are NOT eligible for logo under the Microsoft* Motherboard logo program)
Board AA #/Fab	PBA: D87491-402
Board Manufacturer	Intel Corporation
Board Model	Intel® Server Board S5400SF
Northbridge* Chipset Manufacturer	Intel Corporation
Northbridge Chipset Model	Intel® 5400 Memory Controller Hub
Southbridge* Chipset Manufacturer	Intel Corporation
Southbridge Chipset Model	Intel® 6321ESB I/O Controller
BIOS	
BIOS Manufacturer	AMI*
BIOS Version	S5400.86B.06.00.0026
BIOS Date	4/8/2008
BIOS URL (For Updates)	htttp://support.intel.com/support/motherboards/server/S5400SF/

Bus Types			
	Check all that Apply		**Check all that Apply**
PS/2	☑	AGP*	☐
1394	☐	PCCard* (16-bit)	☐
CF (Compact Flash)	☐	CardBus* (32-bit)	☐
PCI	☑	USB	☐
Mini-PCI	☐	USB 2.0	☑
AMR	☐	InfiniBand*	☐
ACR	☐	Bluetooth*	☐
COM (Serial)	☑	PCI Express	☑
Integrated Components			
	Check all that Apply		**Check all that Apply**
Audio	☐	Display	☑
IDE	☑	Networking	☑
SCSI	☐	RAID	☑
Modem	☐	Bluetooth*	☐

Onboard Integrated Devices and Drivers

Data in this section reflects system configuration at the time of WLP submission. The latest drivers for the Intel® Server System SR1560SFHS-SATA are available for download at:

http://support.intel.com/support/motherboards/server/S5400SF/

Technology	OS	Version
Intel® S5400/ESB2-E Chipset The chipset contains two main components: the Memory Controller Hub (MCH) for the host bridge and the I/O controller hub for the I/O sub-system. The chipset uses the Enterprise South Bridge (ESB2-E) for the I/O controller hub.	**Windows Server 2008** **Windows Server 2008 – 64Bit**	OS Embedded OS Embedded
LSI 1064e SAS Controller (IO Module)	**Windows Server 2008** **Windows Server 2008 – 64Bit**	OS Embedded OS Embedded
Embedded SATA (ESB2-E) Controller Legacy (Enhanced) mode	**Windows Server 2008** **Windows Server 2008 – 64Bit**	8.9.0518.2007 (OS Embedded) 8.9.0518.2007 (OS Embedded)
LAN 2 X Intel PRO/1000-EB Server Network Connection Supports 10/100/1000 and I/O Accelerated Technology (Intel 82563GB Controller)	**Windows Server 2008** **Windows Server 2008 – 64Bit**	9.12.16.0 (Pkg 12.4) 9.12.16.0 (Pkg 12.4)

I/O Accelerated Technology	**Windows Server 2008** **Windows Server 2008 – 64Bit**	1.2.79.9 (Pkg 12.4) 1.2.79.9 (Pkg 12.4)
Display ATI* ES1000 SVGA PCI video controller with 16 MB of video memory	**Windows Server 2008** **Windows Server 2008 – 64Bit**	8.240.50.3000 8.240.50.3000
Intel® ESG-SHV backplane (Null driver)	**Windows Server 2008** **Windows Server 2008 – 64Bit**	6.0.6001.18000 6.0.6001.18000

Product Data for HCL: Completion of WLP

Data in this section reflects product data for HCL at time of WLP submission.

Product Data		
Product Name	Intel® Server Board S5400SF	
Additional Product Names	Intel® Server System SR1560SFHS-SATA Intel® Server System SR1560SF-Fixed	
Supported Platforms		
	Check Tested	**Comments**
Windows Server 2008, Standard Edition	☑	
Windows Server 2008, Standard Edition x64	☑	
Windows Server 2008, Enterprise Edition	☑	
Windows Server 2008, Enterprise Edition x64	☑	
Windows Server 2008, DataCenter	☑	
Windows Server 2008, DataCenter x64	☑	

Hardware Compatibility Tests Used

Microsoft* Windows Hardware Driver Central Server Testing Home Page: http://www.microsoft.com/whdc/hwtest/system/default.mspx.

Please check this website regularly for test kit updates.

Operating Systems	Notes	Hardware Compatibility Tests (HCT)
Windows Server 2008	DTM 1.2 Test Procedures and Readme files for Windows XP SP2, Windows vista, Windows Server 2003, and Windows Server 2008 (4.95MB) http://www.microsoft.com/whdc/winlogo/wlk/default.mspx	DTM 1.2 (Windows XP, Windows Vista, Windows Server 2003, and Windows Server 2008) Test Kit (4.95MB) http://www.microsoft.com/whdc/winlogo/wlk/default.mspx
	Windows Server Marketplace	http://www.windowsservercatalog.com/default.aspx

Errata and Contingencies

Microsoft* System DTM Errata list is available at:
http://www.microsoft.com/whdc/hwtest/search/results.aspx?type=errata&key=system&kit=-1

Operating System	Identification Number	Title
Windows Server 2008		*No errata applied to this submission*

Submission Readme File

Effective May 1, 2002, the new Microsoft standardized Readme form will be required for all hardware submissions that include any of the following testing exceptions:

- Test failures
- Tests not run
- Missing test logs
- Inconclusive test results

All testing exceptions must be identified with a valid Errata ID, Incident ID, or Contingency ID provided by WHQL. The new Microsoft standardized Readme file is available for download at:
http://www.microsoft.com/whdc/winlogo/WLK/default.mspx

Testing Exceptions for S5400SF-SATA Windows Server 2008

Data in this section reflects product data and test exceptions listed in section 2 of the Intel® Server System SR1560SF-Fixed Readme file at time of WLP First-Time submission ID Number **1291685**.

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1078
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	The Bit 5 (Surprise Down Error Severity) in the Uncorrectable Error Severity register (offset Ch) in the Advanced Error Reporting Capability table must be read-only and always return 1 if the Bit 5 (Surprise Down Error Mask) in the Uncorrectable Error Mask Register in the Advanced Errror Reporting Capability table is not implemented	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	887
Failing test name	PCI Harware Compliance Test for Systems Running Windows Vista (PCIHCT)	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Bit 10 (Interrupt Disable) in the Command register (offset 4h) in the Header table must be read-writable if the device supports an interrupt.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1113
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Bit 5 (Retrain Link) in the Link Control register (offset 10h) in the PCI Express Capability table must always return 0 on reads even though it is read-write.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	316
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Bit range 15:8 (Bus Number)in the PCI-X Bridge Status register (offset 4h) in the PCI-X Capability table must be read-only. RESOLUTION: The following assertion failure is allowed EBA19FF0-AB40-4D74-AC05-4ABE22D356BD	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	317
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Header Type 1 Registers failure due to a PCI Compliance test issue RESOLUTION: The following assertion failure is allowed 60BDF3F8-01D2-4B58-8A14-04DA4C1B694A	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1115
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	PCI Compliance - Bit 3 (Read Completion Boundary) in the Link Control register (offset 10h) in the PCI Express Capability table must be read-only and always return 0 for switch ports. RESOLUTION: The following PCI Compliance assertion failure is allowed **9A275B03**-1072-43D6-B034-3DD306D24324	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1080
Failing test name	PCI Hardware Compliance Test	

Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	PCI Compliance test causes the system to hang after testing Power Management capability of the graphics devices. This occurs after the device are put into various D-states and then recovered to D0 state. Cause: The AMD/ATI graphics devices require that the VBIOS be re-posted after transition to various D-states and recovery to D0 state which the PCIHCT doesn't do.
Additional information (for example, test system in a multiple system configuration)	

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1114
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	According to the PCI Express Base Specification, Rev 2.0 Section 7.8.8, this bit field is undefined when the link is not up. If there is not PCIe device behind the bridge, then the link can NOT be up and therefore the field is undefined. Bit 13 in the same register (Link Status) can be used to determine if the link is active (up). The PCIHCT uses the Presence Detect State bit of the Slot Status register to determine whether a child device is present. However, the Presence Detect State bit only returns valid data if the Slot Implemented bit is set (bit 8 of PCIe capabilities register). If the PCIe root port or downstream port will never have a device behind it, the Slot Implemented bit is cleared to 0. Per the spec, PDS will always be 1 when the Slot Implemented bit is clear. Therefore PDS can not be used to determine device presence when the SI bit is clear.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	474
Failing test name	PCI Hardware Compliance Test	

Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	According to the PCI Express Base Specification, Rev 2.0 Section 7.8.8, this bit field is undefined when the link is not up. If there is not PCIe device behind the bridge, then the link can NOT be up and therefore the field is undefined. Bit 13 in the same register (Link Status) can be used to determine if the link is active (up). The PCIHCT uses the Presence Detect State bit of the Slot Status register to determine whether a child device is present. However, the Presence Detect State bit only returns valid data if the Slot Implemented bit is set (bit 8 of PCIe capabilities register). If the PCIe root port or downstream port will never have a device behind it, the Slot Implemented bit is cleared to 0. Per the spec, PDS will always be 1 when the Slot Implemented bit is clear. Therefore PDS can not be used to determine device presence when the SI bit is clear.
Additional information (for example, test system in a multiple system configuration)	

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1166
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	PCI Compliance - Bit 20 (Data Link Layer Link Active Reporting Capable) in the Link Capabilities register (offset Ch) in the PCI Express Capability table must be read-only. RESOLUTION: The following PCI Compliance test assertion failure is allowed ABC66DC4-D88C-496E-845D-63B7F9A05176.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008	Errata	1110
Failing test name	BitLocker Drive Encryption BIOS Interface Logo Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	For platforms without Trusted Platform Modules (TPM), the WLK 1.1 kit has an issue which randomly causes the BitLocker Drive Encryption BIOS Interface Logo Test to fail.	
Additional information (for example, test system in a multiple system configuration)		

Testing Exceptions for Intel® Server System SR1560SF-Fixed Windows Server 2008 64 Bit

Data in this section reflects product data and test exceptions listed in section 2 of the Intel® Server System SR1560SF-Fixed Readme file at time of WLP First-Time submission ID Number **1291685**.

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	1078
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	The Bit 5 (Surprise Down Error Severity) in the Uncorrectable Error Severity register (offset Ch) in the Advanced Error Reporting Capability table must be read-only and always return 1 if the Bit 5 (Surprise Down Error Mask) in the Uncorrectable Error Mask Register in the Advanced Errror Reporting Capability table is not implemented	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	887
Failing test name	PCI Harware Compliance Test for Systems Running Windows Vista (PCIHCT)	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Bit 10 (Interrupt Disable) in the Command register (offset 4h) in the Header table must be read-writable if the device supports an interrupt.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	1113
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Bit 5 (Retrain Link) in the Link Control register (offset 10h) in the PCI Express Capability table must always return 0 on reads even though it is read-write.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	316
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Bit range 15:8 (Bus Number)in the PCI-X Bridge Status register (offset 4h) in the PCI-X Capability table must be read-only. RESOLUTION: The following assertion failure is allowed EBA19FF0-AB40-4D74-AC05-4ABE22D356BD	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	317
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	Header Type 1 Registers failure due to a PCI Compliance test issue RESOLUTION: The following assertion failure is allowed 60BDF3F8-01D2-4B58-8A14-04DA4C1B694A	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	1115
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	PCI Compliance - Bit 3 (Read Completion Boundary) in the Link Control register (offset 10h) in the PCI Express Capability table must be read-only and always return 0 for switch ports. RESOLUTION: The following PCI Compliance assertion failure is allowed **9A275B03**-1072-43D6-B034-3DD306D24324	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	324

Failing test name	PCI Hardware Compliance Test
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	PCI Compliance - Bit 20 (Data Link Layer Link Active Reporting Capable) in the Link Capabilities register (offset Ch) in the PCI Express Capability table must be read-only. RESOLUTION: The following PCI Compliance test assertion failure is allowed ABC66DC4-D88C-496E-845D-63B7F9A05176.
Additional information (for example, test system in a multiple system configuration)	

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	1080
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	PCI Compliance test causes the system to hang after testing Power Management capability of the graphics devices. This occurs after the device are put into various D-states and then recovered to D0 state. Cause: The AMD/ATI graphics devices require that the VBIOS be re-posted after transition to various D-states and recovery to D0 state which the PCIHCT doesn't do.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	1114
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	According to the PCI Express Base Specification, Rev 2.0 Section 7.8.8, this bit field is undefined when the link is not up. If there is not PCIe device behind the bridge, then the link can NOT be up and therefore the field is undefined. Bit 13 in the same register (Link Status) can be used to determine if the link is active (up). The PCIHCT uses the Presence Detect State bit of the Slot Status register to determine whether a child device is present. However, the Presence Detect State bit only returns valid data if the Slot Implemented bit is set (bit 8 of PCIe capabilities register). If the PCIe root port or downstream port will never have a device behind it, the Slot Implemented bit is cleared to 0. Per the spec, PDS will always be 1 when the Slot Implemented bit is clear. Therefore PDS can not be used to determine device presence when the SI bit is clear.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	474
Failing test name	PCI Hardware Compliance Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	According to the PCI Express Base Specification, Rev 2.0 Section 7.8.8, this bit field is undefined when the link is not up. If there is not PCIe device behind the bridge, then the link can NOT be up and therefore the field is undefined. Bit 13 in the same register (Link Status) can be used to determine if the link is active (up). The PCIHCT uses the Presence Detect State bit of the Slot Status register to determine whether a child device is present. However, the Presence Detect State bit only returns valid data if the Slot Implemented bit is set (bit 8 of PCIe capabilities register). If the PCIe root port or downstream port will never have a device behind it, the Slot Implemented bit is cleared to 0. Per the spec, PDS will always be 1 when the Slot Implemented bit is clear. Therefore PDS can not be used to determine device presence when the SI bit is clear.	
Additional information (for example, test system in a multiple system configuration)		

Operating system (Windows XP, Windows 2000, etc.)	**Failure type** (Contingency, Errata, Incident)	**ID number**
Windows Server 2008 x64	Errata	1110
Failing test name	BitLocker Drive Encryption BIOS Interface Logo Test	
Applicable error message (Type **N/A** if the error message or failing text is excessive or if there is no text)	For platforms without Trusted Platform Modules (TPM), the WLK 1.1 kit has an issue which randomly causes the BitLocker Drive Encryption BIOS Interface Logo Test to fail.	
Additional information (for example, test system in a multiple system configuration)		

Additional information entered in section 3 of the Intel® Server System SR1560SF-Fixed Readme file at time of WLP submission ID 1291685.

Errata 1175 (1176, 1177) The Virtualization – Virtual Machine Detect Test is incorrectly available to run in the Server category for non virtualization featured server.

Appendix A – Submission History

Microsoft "Designed for Windows*" logo submission history for the Intel® Server System S1560SF-SATA:

Submission ID	Type	Date	OS Qualified	Processor Speeds	Board Revision	BIOS Version
1276454	First-Time	12/17/07	Windows 2003 EE SP2 - 32 & 64 Bit		402	S5400.86B.06.00.23 11/11/2007
1291685	First-Time	5/19/2008	Windows Server 2008 - 32 & 64 Bit		402	S5400.86B.06.00.0026 4/8/2008