



Motherboard Logo Program (MLP)

Intel® Desktop Board

DX58SO

MLP Report

8/10/2009

Purpose:

This report describes the DX58SO Motherboard Logo Program testing run conducted by Intel Corporation.

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Contents

- Introduction 4**
 - Terms and Definitions..... 4
- Desktop Board Configuration 5**
 - Desktop Board DX58S0 Final Configuration Report: Completion of MLP 5
 - Board Information 5
 - Product Code..... 5
 - Processor 5
 - Power Management 5
 - Operating System Tested 5
 - Onboard Integrated Devices and Driver 6
 - Windows Logo Kits Used (WLK) 6
 - Errata and Contingencies 7
 - Test Notes 8

Introduction

Terms and Definitions

Term	Definitions
WHQL	Windows* Hardware Qualification Lab
WLK	Windows Logo Kits
MLP	Motherboard Logo Program. For further information see: http://www.microsoft.com/whdc/hwtest/default.msp
AP Machine	Audio Precision Machine
Winqual	Windows Qualification
MSFT Tested Product List	Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&q=s

Desktop Board Configuration

Desktop Board DX58S0 Final Configuration Report: Completion of MLP

Data in this section reflects system configuration at time of MLP submission.

Board Information

Product Code ¹	BIOS String/Model	Technologies NOT Logo'd (yet)
DX58S0	SOX5810J.86A.4158.2009.0622.2125	N/A - all technologies logo'd
Processor		
Speed	3.20 GHz	
Family	Intel® Core™ i7 Extreme Edition	
Bus Speed	6.4 GT/s	
Motherboard		
Board AA #	E68555	
Board FAB #	503	
* This report applies to the production FAB revision; Please consult your Intel Corporation representative to clarify the motherboard revision you intend to perform logo testing if not the same.		
System Memory		
Speed	Triple Channels, DDR3, 1333MHz	
Memory Type	DIMM	
Connector Type	DDR3, 240 Pin	
Power Management		
BIOS Default	S3	
Operating System Tested		
	Check Tested	Comments
Windows 7 and 64-bit	<input checked="" type="checkbox"/>	Windows 7 Ultimate
Windows Vista and 64-bit	<input checked="" type="checkbox"/>	Vista Ultimate with Service Pack 2
Windows Vista Basic and 64-bit	<input type="checkbox"/>	Vista Basic with Service Pack 2

¹ These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Onboard Integrated Devices and Driver for Vista 32-bit and 64-bit

Technology	OS	Version	Package version
Chipset Update Utility Intel® Chipset Software Utility	Windows Vista	9.0.0.1005	9.1.0.1007
	Windows Vista 64-bit	9.0.0.1005	9.1.0.1007
Audio Realtek	Windows Vista	6.0.1.5672	5672
	Windows Vista 64-bit	6.0.1.5672	5672
LAN Intel® PRO Network Connections	Windows Vista	9.52.9.0	13.1
	Windows Vista 64-bit	9.52.9.0	13.1
CIR Winbond Consumer Infrared	Windows Vista	7.0.65.1002	7.0.65.1006
	Windows Vista 64-bit	7.0.65.1002	7.0.65.1006

Onboard Integrated Devices and Driver for Windows 7 32-bit and 64-bit

Technology	OS	Version	Package version
Chipset Update Utility Intel® Chipset Software Utility	Windows 7	6.1.7229.0	Inbox Driver
	Windows 7 64-bit	6.1.7229.0	Inbox Driver
Audio Realtek	Windows 7	6.1.7229.0	Inbox Driver
	Windows 7 64-bit	6.1.7229.0	Inbox Driver
LAN Intel® PRO Network Connections	Windows 7	10.0.2.2	Inbox Driver
	Windows 7 64-bit	10.0.2.2	Inbox Driver

Windows Logo Kits Used (WLK)

Microsoft website: <http://www.microsoft.com/whdc/DevTools/WDK/DTM.msp>

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

Operating Systems	Notes	WHQL Testkit
Windows 7 Windows 7 64-bit	WLK1.4 for Windows 7	WLK1.4 for Windows 7
Windows Vista Windows Vista 64-bit	WLK1.4 for Windows Vista SP2	WLK1.4 for Windows Vista SP2

Errata and Contingencies

Operating System	Failing Test	Expiry Date	ID Number	Type	Error Description
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	12/31/2009	401	Erratum	The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a.
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	6/1/2010	1241	Erratum	This happens because the PCI Compliance test assumes that if the Data Link Layer Link Active Reporting Capable bit in the Link Capabilities register for a given PCIe port is set then that indicates that the Data Link Layer Link Active bit will also be set. This is an incorrect assumption because the Data Link Layer Link Active bit can be reset when there is no device below the port. This assertion needs to be removed from the PCIHCT. The current architecture of the PCIHCT prevents it from knowing whether devices exist below a bridge/port.
Windows 7 Windows 7 64-bit	1)Class Drive Fidelity Test - Win7 (System, Manual) 2)Fidelity Test - Win7 (System, Manual)	12/31/2009	1238	Erratum	Fidelity Test now includes a "Render Power Transition" test. This preview filter covers all errors in this new test of SYSFUND-0050.
Windows 7 Windows 7 64-bit	1)Class Drive Fidelity Test - Win7 (System, Manual) 2)Fidelity Test - Win7 (System, Manual)	12/1/2009	966	Erratum	Fidelity Test includes a "System Activity Test" that verifies audio fidelity is not compromised during system activity. It uses an activity generator called rws.exe (Real World Stress) which has a bug that causes it to hit an access violation. When this happens it exits with return code 0xC0000005, or -1073741819.
Windows 7 Windows 7 64-bit	1)AC3 Test (System) 2)Class Drive AC3 Test - Win7 (System)	6/1/2010	1256	Erratum	Run AC3 test on a system with the Microsoft HD Audio class driver installed. Expected results: All AC3 kernel streaming data ranges should advertise MinimumBitsPerSample = 16 and MaximumBitsPerSample = 16. Actual results: HD Audio class driver sometimes advertises MaximumBitsPerSample = 24.
Windows 7 Windows 7 64-bit	WDDM CCD Test for Forced Projection	6/1/2010	1268	Erratum	The WDDM CCD Test for forced projection test fails due to not recognizing SDTV dongle as a forcible target.
Windows 7 Windows 7 64-bit	UAA Test - Win7 (System)	12/1/2009	513	Erratum	UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure.

Test Notes

Operating System	Test	Description
Windows 7 and Vista	BIOS download	Internal: http://bios.intel.com/downloads/ External: http://www.intel.com/ click on Support and Download
Windows 7 and Vista	BIOS setup	Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component (Except CIR) Drive Configuration: Set to IDE Chipset Configuration: Enable HPET ACPI Suspend State: Set to <S3 State> Boot Device Priority: set <Hard Disk Driver> to first Note: Enhanced Consumer IR (CIR) component is not supported under Windows7.
Windows 7 and Vista filter update	WLK WHQL test	http://winqual.microsoft.com/member/SubmissionWizard/LegalExemptions/filterupdates.cab
Special H/W that use to PASS the test	None	None