Motherboard Logo Program (MLP)

Intel® Desktop Board DQ57TM

MLP Report

2/10/2010

Purpose:

This report describes the DQ57TM Motherboard Logo Program testing run conducted by Intel Corporation.

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Introduction

Terms and Definitions

Term	Definitions
WHQL	Windows* Hardware Qualification Lab
WLK	Windows Logo Kits
MLP	Motherboard Logo Program. For further information see: http://www.microsoft.com/whdc/hwtest/default.mspx
AP Machine	Audio Precision Machine
Winqual	Windows Qualification
MSFT Tested Product List	Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&g=s

Desktop Board Configuration

Desktop Board DQ57TM Final Configuration Report: Completion of MLP

Data in this section reflects system configuration at time of MLP submission.

Board Information

Product Code[1]	BIOS String/Model	Technologies NOT Logo'd (yet)
DQ57TM	TMIBX10H.86A.0025.2009.1202.2128	N/A - all technologies logo'd
Processor		
Speed	3.46GHz	
Family	Intel® Core(TM) i5 670	
Bus Speed	1333Mhz	
Motherboard		
Board AA #	E70931	
Board FAB #	302	
* This report applies to the production FAB revision; Please consult your Intel Corporation representative to clarify the motherboard revision you intend to perform logo testing if not the same.		
System Memory		
Speed	Dual Channel, DDR3, 1333MHz	
Memory Type	DIMM	
Connector Type	DDR3, 240 Pin	
Power Management		
BIOS Default	S3	
Operating System Tested		
	Check Tested	**Comments**
Windows 7 and 64-bit	☑	Windows 7 Ultimate
Windows Vista and 64-bit	☐	Vista Ultimate with Service Pack 2
Windows Vista Basic and 64-bit	☐	Vista Basic with Service Pack 2

[1] These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Onboard Integrated Devices and Driver for Windows 7 32-bit and 64-bit

Technology	OS	Version
Chipset Update Utility Intel® Chipset Software Utility	**Windows 7**	9.1.1.1020
	Windows 7 64-bit	9.1.1.1020
GFX Intel® Graphics Media Accelerator	**Windows 7**	8.15.10.2008
	Windows 7 64-bit	8.15.10.2008
Audio Realtek	**Windows 7**	6.0.1.5977
	Windows 7 64-bit	6.0.1.5977
LAN Intel	**Windows 7**	11.2.19.0
	Windows 7 64-bit	11.2.19.0
MEI Intel	**Windows 7**	6.0.0.1179
	Windows 7 64-bit	6.0.0.1179

Windows Logo Kits Used (WLK)

Microsoft website: http://www.microsoft.com/whdc/DevTools/WDK/DTM.mspx

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

Operating Systems	Notes	WHQL Testkit
Windows 7 Windows 7 64-bit	WLK1.5 for Windows 7	WLK1.5 for Windows 7

Errata and Contingencies

Operating System	Failing Test	Expiry date	ID Number	Type	Error Description
Windows 7 Windows 7 64	Class Driver AC3 Test - Win7 (System)	6/1/2010	1256	Erratum	Run AC3 test on a system with the Microsoft HD Audio class driver installed. Expected results: All AC3 kernel streaming data ranges should advertise MinimumBitsPerSample = 16 and MaximumBitsPerSample = 16. Actual results: HD Audio class driver sometimes advertises MaximumBitsPerSample = 24.
	PCI Hardware Compliance Test For Systems	6/1/2010	1241		This happens because the PCI Compliance test assumes that if the Data Link Layer Link Active Reporting Capable bit in the Link Capabilities register for a given PCIe port is set then that indicates that the Data Link Layer Link Active bit will also be set. This is an incorrect assumption because the Data Link Link Layer Link Active bit can be reset when there is no device below the port. This assertion needs to be removed from the PCIHCT. The current architecture of the PCIHCT prevents it from knowing whether devices exist below a bridge/port.
		2/8/2010	401		The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a.
	UAA Test - Win7 (System)	6/1/2010	513		UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure.
	TPM TCG OS Interface Test	6/1/2015	1775		Errata 1775 The Physical Presence Lifetime Lock on the TPM 1.2 device was not set during the manufacturing process. Once set, the lifetime lock is permanent, so occasionally platform manufacturers wait until late in platform development to set the value. To pass the TCG TPM Integration Test, the value must be set.

Test Notes

Operating System	Test	Description
Windows 7 and Vista	BIOS download	Internal: http://bios.intel.com/downloads/ External: http://www.intel.com/ click on Support and Download
Windows 7 and Vista	BIOS setup	Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component Drive Configuration: Set to IDE Chipset Configuration: Enable HPET ACPI Suspend State: Set to <S3 State> Boot Device Priority: set <Hard Disk Driver> to first
Windows 7 and Vista filter update	WLK WHQL test	http://winqual.microsoft.com/member/SubmissionWizard/LegalExemptions/filterupdates.cab
Special H/W that use to PASS the test	None	None