



Intel® Desktop Board DQ965WC Specification Update

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Order Number: D63341-003US

The Intel® Desktop Board DQ965WC may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board DQ965WC may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

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REVISION HISTORY

Date of Revision	Version	Description
September 6, 2006	-001	This document is the first Specification Update for the Intel® Desktop Board DQ965WC.
November 2006	-002	Added Specification Changes 1.
February 2007	-003	Added Specification Changes 2.



PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board DQ965WC Technical Product Specification* (Order Number D56018). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- *Intel® Pentium® 4 Processor 6x1Δ Sequence Specification Update* (Order Number 310309)
- *Intel® Pentium® 4 Processor on 90 nm Process Specification Update* (Order Number 302352)
- *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199)
- *Intel® Pentium® D Processor 900Δ Sequence Specification Update* (Order Number 310307)
- *Intel® Pentium® D Processor 800Δ Sequence Specification Update* (Order Number 306832)
- *Intel® Core™2 Extreme Processor X6800Δ and Intel® Core™2 Duo Desktop Processor E6000Δ Sequence Specification Update* (Order Number 313279)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 965 Express Chipset Family Specification Update* (Order Number 313054) for specification updates concerning the 82Q965 GMCH Controller and that may apply to the desktop board DQ965WC. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub8 (ICH8) Family Specification Update* (Order Number 313057) for specification updates concerning the 82801HO I/O Controller Hub and that may apply to the desktop board DQ965WC. Unless otherwise noted in this document, it should be assumed that any ICH8 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board DQ965WC's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the
Intel[®] Desktop Board DQ965WC**



GENERAL INFORMATION

Basic Desktop Board DQ965WC Identification Information

AA Revision	BIOS Revision	Notes
D41834-500	CO96510J.86A.4713.2006.0828.1752	1,2
D39923-500	CO96510J.86A.4713.2006.0828.1752	1,2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 965 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82Q965	C2	SL9QZ
82801HO	B0	SL9MM



Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DQ965WC. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Changes to Section 1.7.2 Audio Connectors
2	Doc	Changes to Section 1.9.1 Intel® 82566DM Gigabit Ethernet Controller
NO.	PLANS	ERRATA
		There is not characterized erratum for this product



SPECIFICATION CHANGES

1. *The following note will be added to the Technical Product Specification in Section 1.7.2 Audio Connectors:*

INTEGRATOR'S NOTES

Electrostatic discharge (ESD) can damage desktop board components. Front-panel connectors should provide sufficient protection to prevent ESD damage to components inside the chassis enclosure.

2. *Section 1.9.1 of the Technical Specification will be updated in its entirety as follows to remove reference to Jumbo frame support.*

1.9.1 INTEL® 82566DM GIGABIT ETHERNET CONTROLLER

The Intel® 82566DM Gigabit Ethernet Controller supports the following features:

- PCI Express link
- 10/100/1000 IEEE 802.3 compliant
- Compliant to IEEE 802.3x flow control support
- TCP, IP, UDP checksum offload
- Transmit TCP segmentation
- Advanced packet filtering
- Full device driver compatibility
- PCI Express Power Management Support