



# Intel® Desktop Board DP965LT Specification Update

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The Intel® Desktop Board DP965LT may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board DP965LT may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

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## REVISION HISTORY

<b>Date of Revision</b>	<b>Version</b>	<b>Description</b>
June 19, 2006	-001	This document is the first Specification Update for the Intel® Desktop Board DP965LT.
October 2006	-002	Added Core 2 Duo information and updated chipset specification update information. Added Erratum 1 & 2.
November 2006	-003	Added Specification Changes 1.
February 2007	-004	Added Specification Changes 2.



## PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board DP965LT Technical Product Specification* (Order Number D56017). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- *Intel® Core™2 Extreme Processor X6800Δ and Intel® Core™2 Duo Desktop Processor E6000Δ Sequence Specification Update* (Order Number 313279)
- *Intel® Pentium® 4 Processor 6x1Δ Sequence Specification Update* (Order Number 310309)
- *Intel® Pentium® 4 Processor on 90 nm Process Specification Update* (Order Number 302352)
- *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® P965 Broadwater Chipset Specification Update* (Order Number 313054) for specification updates concerning the 82P965 MCH Controller and that may apply to the desktop board DP965LT. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub8 (ICH8) Family Specification Update* (Order Number 313057) for specification updates concerning the 82801HB I/O Controller Hub and that may apply to the desktop board DP965LT. Unless otherwise noted in this document, it should be assumed that any ICH8 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

## Nomenclature

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board DP965LT's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

# Specification Update for the Intel® Desktop Board DP965LT







## GENERAL INFORMATION

**Basic Desktop Board DP965LT Identification Information**

AA Revision	BIOS Revision	Notes
D41694-204	MQ96510J.86A.725	1,2
D41694-205	MQ96510J.86A.784	1,2
D41694-206	MQ96510J.86A.0816	1,2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82P965 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82P965	C1	SL9NU
82P965	C2	SL9QX
82801HB	B0	SL9MN



### Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DP965LT. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

#### CODES USED IN SUMMARY TABLE

- Doc: Document change or update that will be implemented.
- PlanFix: This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
- Fixed: This erratum has been previously fixed.
- NoFix: There are no plans to fix this erratum.
- Shaded: This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Changes to Section 1.7.2 Audio Connectors
2	Doc	Changes to Section 1.8.1 Intel® 82566DC Gigabit Ethernet Controller
NO.	PLANS	ERRATA
1	Fixed	Doing extended Boot cycling will occasionally observe board hang at POST code 46
2	Fixed	Intel® Quiet System Technology fails to initialize properly



## SPECIFICATION CHANGES

1. *The following note will be added to the Technical Product Specification in Section 1.7.2 Audio Connectors:*

### INTEGRATOR'S NOTES

*Electrostatic discharge (ESD) can damage desktop board components. Front-panel connectors should provide sufficient protection to prevent ESD damage to components inside the chassis enclosure.*

2. *Section 1.8.1 of the Technical Specification will be updated in its entirety as follows to remove reference to Jumbo frame support.*

#### 1.8.1 INTEL® 82566DC GIGABIT ETHERNET CONTROLLER

The Intel® 82566DC Gigabit Ethernet Controller supports the following features:

- PCI Express link
- 10/100/1000 IEEE 802.3 compliant
- Compliant to IEEE 802.3x flow control support
- TCP, IP, UDP checksum offload
- Transmit TCP segmentation
- Advanced packet filtering
- Full device driver compatibility
- PCI Express Power Management Support



## ERRATA

### **1. *Doing extended Boot Cycling will occasionally observe board hang at POST code 46***

**PROBLEM:** When doing Boot Cycling testing the board can experience a hard hang at POST Code 46

**IMPLICATION:** If the board fails during Boot cycling due to this erratum the customer will observe a hung system and will need to reset the system to continue.

**WORKAROUND:** None

**STATUS:** Fixed in BIOS MQ96510J.86A.0816.

### **2. *Intel® Quiet System Technology fails to initialize properly***

**PROBLEM:** Intel® Quiet System Technology fails to initialize and max fan speed results

**IMPLICATION:** If the ME-HECI controller fails to initialize correctly then the Intel Quiet System Technology will not work and the customer will experience increase system sound levels.

**WORKAROUND:** None

**STATUS:** Fixed in BIOS MQ96510J.86A.0816.