



Intel[®] Desktop Board DG965SS Specification Update

Intel Confidential

Release Date: November 2007

Order Number: D65906-004US

The Intel[®] Desktop Board DG965SS may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DG965SS.	August 7, 2006
-002	Added Specification Changes 1.	November 2006
-003	Added Specification Changes 2.	February 2007
-004	Updated the General Information table.	November 2007

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL® PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER, AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. INTEL PRODUCTS ARE NOT INTENDED FOR USE IN MEDICAL, LIFE SAVING, LIFE SUSTAINING APPLICATIONS.

Intel may make changes to specifications and product descriptions at any time, without notice.

Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them.

The Intel® Desktop Board DG965SS may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications before placing your product order.

Copies of documents which have an ordering number and are referenced in this document, or other Intel literature, may be obtained from:

Intel Corporation
P.O. Box 5937
Denver, CO 80217-9808

or call in North America 1-800-548-4725, Europe 44-0-1793-431-155, France 44-0-1793-421-777, Germany 44-0-1793-421-333, other Countries 708-296-9333

Intel, the Intel logo, Intel Core, and Pentium are trademarks of Intel Corporation in the United States and other countries.

* Other names and brands may be claimed as the property of others.

Copyright © 2007 Intel Corporation.

Contents

Specification Update for the Intel® Desktop Board DG965SS

- Terminology 5
- General Information 5
- Summary of Changes 6
- Specification Changes 6

Specification Update for the Intel[®] Desktop Board DG965SS

This document is an update to the specifications contained in the *Intel[®] Desktop Board DG965SS Technical Product Specification* (Order Number D56010). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- *Intel[®] Core[™]2 Extreme Processor X6800 Δ and Intel[®] Core[™]2 Duo Desktop Processor E6000 Δ Sequence Specification Update* (Order Number 313279)
- *Intel[®] Pentium[®] 4 Processor 6x1 Δ Sequence Specification Update* (Order Number 310309)
- *Intel[®] Pentium[®] 4 Processor on 90 nm Process Specification Update* (Order Number 302352)
- *Intel[®] Pentium[®] 4 Processor Specification Update* (Order Number 249199)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel[®] P965 Broadwater Chipset Specification Update* (Order Number 313054) for specification updates concerning the 82G965 MCH Controller and that may apply to the desktop board DG965GS. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel[®] IO Controller Hub8 (ICH8) Family Specification Update* (Order Number 313057) for specification updates concerning the 82801HB I/O Controller Hub and that may apply to the desktop board DG965GS. Unless otherwise noted in this document, it should be assumed that any ICH8 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board DG965SS Identification Information

AA Revision	BIOS Revision	Notes
D41678-309	MQ96510J.86A.1687	1,2
D41678-308	MQ96510J.86A.1687	1,2
D41678-307	MQ96510J.86A.1676	1,2
D41678-306	MQ96510J.86A.0816	1,2
D41678-305	MQ96510J.86A.0816	1,2
D41678-304	MQ96510J.86A.0816	1,2
D41678-303	MQ96510J.86A.0816	1,2

Notes:

1. The AA number is found on a small label on the component side of the board.
2. The 965 Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82G965	C2	L9R5
82801HB	B0	L9MN

Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DG965SS. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

No.	Plans	Specification Changes
1	Doc	Change to Section 1.7.2 Audio Connectors
2	Doc	Change to Section 1.8.1 82566DC Gigabit Ethernet Controller
No.	Plans	Errata
		There is not characterized erratum for this product

Specification Changes

1. The following note will be added to the Technical Product Specification in Section 1.7.2 Audio Connectors:

INTEGRATOR'S NOTES

Electrostatic discharge (ESD) can damage desktop board components. Front-panel connectors should provide sufficient protection to prevent ESD damage to components inside the chassis enclosure

2. Section 1.8.1 of the Technical Product Specification will be updated in its entirety as follows to remove reference to Jumbo frame support:

1.8.1 Intel® 82566DC Gigabit Ethernet Controller

The Intel® 82566DC Gigabit Ethernet Controller supports the following features:

- PCI Express link
- 10/100/1000 IEEE 802.3 compliant
- Compliant to IEEE 802.3x flow control support
- TCP, IP, UDP checksum offload
- Transmit TCP segmentation
- Advanced packet filtering
- Full device driver compatibility
- PCI Express Power Management Support