



Intel® Desktop Board D915GHA Specification Update

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The Intel® Desktop Board D915GHA may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D915GHA may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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CONTENTS

REVISION HISTORY	v
PREFACE	vi
Specification Update for the Intel® Desktop Board D915GHA	
GENERAL INFORMATION	3
ERRATA	5



REVISION HISTORY

Date of Revision	Version	Description
July 2004	-001	This document is the first Specification Update for the Intel® Desktop Board D915GHA.
December 2004	-002	Added Erratum 1, 2.
March 2005	-003	Added Erratum 3.

PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D915GHA Technical Product Specification* (Order number C74530). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D915GHA. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 915G and 915P Chipset: Specification Update* (Order Number 301468) for specification updates concerning the 82915G MCH Controller and that may apply to the desktop board D915GHA. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for specification updates concerning the 82801ER I/O Controller Hub and that may apply to the desktop board D915GHA. Unless otherwise noted in this document, it should be assumed that any ICH 6 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D915GHA's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the
Intel® Desktop Board D915GHA**

GENERAL INFORMATION

Basic Desktop Board D915GHA Identification Information

AA Revision	BIOS Revision	Notes
C67700-200	EV91510A.86A.0213	1-5
C67700-201	EV91510A.86A.0213	1-5
C67700-202	EV91510A.86A.0332	1-5

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82915G Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82915G GMCH	B1	SL7LX
82801FB ICH6	B1	SL7AG

3. Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for errata related to the Pentium 4 processor and that may apply to the desktop board D915GHA.
4. Refer to the *Intel® 915G and 915P Chipset: Specification Update* (Order Number 301468) for errata related to the 82915G MCH that may apply to the desktop board D915GHA.
5. Refer to the *Intel® 915G and 915P Chipset: Specification Update* (Order Number 301468) for errata related to the 82801FB I/O Controller Hub that may apply to the desktop board D915GHA.

Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D915GHA. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
1	Plan Fix	Trusted Platform Module (TPM) may be undetected by Microsoft Windows* operating systems the first time the TPM is initialized
2	Plan Fix	Alert Standard Format (ASF) is non-functional
3	Plan Fix	Wake On LAN may not function after loss and restore of AC power

ERRATA

1. ***Trusted Platform Module (TPM) May Be Undetected By Microsoft Windows* Operating Systems the First Time the TPM is Initialized***

PROBLEM: After enabling TPM in BIOS Setup for the first time and then booting into the Windows operating system, TPM may be missing from Device Manager.

IMPLICATION: TPM may not be available to the operating system for user configuration.

WORKAROUND: When enabling TPM the first time, follow these steps:

1. Enable TPM in BIOS Setup
2. Boot into Windows operating system
3. Shut down Windows operating system
4. Wait 15 seconds
5. Power on the system

STATUS: This issue may be fixed in a future BIOS revision.

2. ***Alert Standard Format (ASF) is Non-Functional***

PROBLEM: BIOS and device driver support is currently not implemented to support Alert Standard Format (ASF).

IMPLICATION: ASF, an interface that provides access and manageability in OS-absent environments, is currently not supported.

WORKAROUND: None.

STATUS: This erratum may be fixed in a future BIOS and driver revision.

3. ***Wake On LAN May Not Function After Loss and Restore of AC Power***

PROBLEM: If AC power is removed, Wake On LAN may not function until the LAN circuit is re-initialized. The sequence of events is as follows:

- AC power removed (AC power cord disconnected, for example)
- AC power restored. +5V Standby voltage is re-applied to the board
- Wake On LAN may not be functional
- Switch power on
- Switch power off to put system back into Standby mode (S5 state)
- Wake On LAN functionality restored

IMPLICATION: Wake on LAN will not function if AC power is interrupted.



WORKAROUND: In the BIOS Setup program, set After Power Failure to “Power On” in the Power Menu.

STATUS: This erratum may be fixed in a future BIOS revision.