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Patents

- » 7,299,379, Cache integrity apparatus, systems, and methods, 11/20/2007
- » 7,286,387, Reducing the effect of write disturbs in polymer memories, 10/23/2007
- » 7,275,135, Hardware updated metadata for non-volatile mass storage cache, 9/25/2007
- » 7,233,880, Adaptive cache algorithm for temperature sensitive memory, 6/19/2007
- » 7,222,052, Temperature adaptive ferro-electric memory access parameters, 5/22/2007
- » 7,218,545, Polymer de-imprint circuit using negative voltage, 5/15/2007
- » 7,203,886, Memory architecture and its method of operation, 4/10/2007
- » 7,184,289, Parallel electrode memory, 2/27/2007
- » 7,168,026, Method and apparatus for preservation of failure state in a read destructive memory, 1/23/2007
- » 7,308,531, Hybrid mass storage system and method, 12/11/2007
- » 7,079,490, Integrated circuit with trace analyzer, 7/18/2006
- » 6,941,423, Non-volatile mass storage cache coherency administrator, 9/6/2005
- » 6,925,015, Stacked memory device having shared bitlines and method of making the same, 8/2/2005
- » 6,922,350, Reducing the effect of write disturbs in polymer, 7/26/2005
- » 6,920,533, System boot time reduction method, 7/19/2005
- » 6,915,376, Host controlled optimization of disk storage devices, 7/5/2005
- » 6,914,853, Mechanism for efficient wearout counters in destructive readout memory, 7/5/2005
- » 6,785,767, Hybrid mass storage system and method with two different types of storage medium, 8/31/2004
- » 6,742,080, Disk block allocation optimization methodology and application, 5/25/2004
- » 6,725,342, Non-volatile mass storage cache coherency apparatus, 4/20/2004
- » 6,437,783, Method and system for simultaneously displaying the throughput on multiple busses, 8/20/2002
- » 6,345,349, Combined memory and mass storage device, 12/5/2002

- » 6,317,875, Application execution performance through disk block relocation, 11/13/2001
- » 6,253,296, Disk block allocation optimization methodology and applications, 6/26/2001
- » 6,023,713, Optimized CD/DVD authoring employing block reallocation, 2/8/2000
- » 6,012,117, Methods and apparatus for arbitrating and controlling arbitration for access to a serial bus, 1/4/2000
- » 6,009,527, Computer system security, 12/28/1999
- » 5,920,896, Reducing operating system start-up/boot time through disk block relocation, 7/6/1999
- » 5,890,205, Optimized application installation using disk block relocation, 3/30/1999
- » 5,862,350, Method and mechanism for maintaining integrity within SCSI bus with hot insertion, 1/19/1999
- » 5,809,337, Mass storage devices utilizing high speed serial communications, 9/15/1998
- » 5,802,069, Implementing mass storage device functions using host processor memory, 9/1/1998
- » 5,787,296, Method and apparatus for reducing power consumption by a disk drive through disk block relocation, 7/28/1998
- » 5,761,458, Intelligent bus bridge for input/output subsystems in a computer system, 6/2/1998
- » 5,548,730, Intelligent bus bridge for input/output subsystems in a computer system, 8/20/1996
- » 5,367,647, Apparatus and method for achieving improved SCSI bus control capacity, 11/22/1994
- » 4,536,836, Detection of sequential data stream, 8/20/1985
- » 4,503,501, Adaptive domain partitioning of cache memory space, 3/5/1985
- » 4,468,730, Detection of sequential data stream for improvements in cache data storage, 8/28/1984
- » 4,430,712, Adaptive domain partitioning of cache memory space, 2/7/1984